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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete if Known		
Application Number	10/773,968	
Filing Date	February 6, 2004	
First Named Inventor	Paul D. Shirley	
Art Unit		
Examiner Name	·	
Attorney Docket Number	MIO 0112 PA/40509.272/00-0265	

U. S. PATENT DOCUMENTS Name of Patentee or Pages, Columns, Lines, Where Cite Examiner **Document Number Publication Date** Initials' MM-DD-YYYY Applicant of Cited Document Relevant Passages or Relevant Figures Appear Number-Kind Code^{2 (d known)} US- 6.548,110 B1 or 04-15-2003 US-US US US-US-LIS US-US-US-HS. US ŪŠ-US-US-TIS

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages	
		Country Code ³ "Number ⁴ " Kind Code ⁵ (if known)	MM-DD-YYYY		Or Relevant Figures Appear	T⁵
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		NON PATENT LITERATURE DOCUMENTS	
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8		MICHAEL QUIRK AND JULIAN SERDA, Semiconductor Manufacturing Technology, November 11, 2003, Pages 357 - 360 Chapter 13, Prentice Hall, Upper Saddle River, New Jersey and Columbus, Ohio	
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